IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In re U.S. Patent Application of | | | | | | |
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| SUGA | AHARA et al. |) | | | | |
| Appli | cation Number: To Be Assigned |) | | | | |
| Filed | : Concurrently Herewith |) | | | | |
| For: | A SPIN TRANSISTOR BASED ON THE SPIN- FILTER EFFECT AND A NONVOLATILE MEMORY USING SPIN TRANSISTORS |) | | | | |
| ATTO | ORNEY DOCKET NO. HIRA.0178 |) | | | | |

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, this Information Disclosure Statement is submitted in the above-identified National Stage U.S. patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper. A copy of each U.S. and foreign patent, or each publication or portion thereof listed or herein identified, is submitted herewith.

CERTIFICATION

This Information Disclosure Statement is submitted with the initial filing of the application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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January 25, 2005

| Form PTO 1449 | ATTY. DOCKET NUMBER HIRA.0178 | Serial NUMBER 522241 To be Assigned 522241 |
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| U.S. Department of Commerce Patent and Trademark Office | APPLICANT SUGAHARA et al. | |
| Information Disclosure Statement by Applicant | FILING DATE Concurrently Herewith | GROUP |

U.S. Patent Documents

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| EXAMINER | DATE CONSIDERED | | | | |

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